

Attorney's Docket No. 2986.P045

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Kenneth S. McElvain

Application No.: 10/758,977

Filing Date: 01/14/2004

For: Circuits with Modular Redundancy
and Methods and Apparatuses for
their Automated Synthesis

Examiner: Not Yet Assigned
Art Unit: 2816

FIRST CLASS CERTIFICATE OF MAILING

I hereby certify that this correspondence is being
deposited with the United States Postal Service as first
class mail with sufficient postage in an envelope
addressed to Commissioner for Patents, P.O. Box 1450,
Alexandria VA 22313-1450

on 07/12/05
Date Judy L. Steinhkraus

Commissioner for Patents
P.O. Box 1450,
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO/SB/08 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant.

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

 X 37 C.F.R. §1.97(b).

 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:

 A statement pursuant to 37 C.F.R. §1.97(e); or

 A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

 37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following:

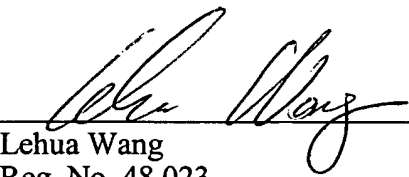
- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
- (2) A check for \$180.00 for the fee under 37 C.F.R. §1.17(p) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

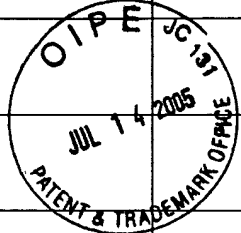
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 7/12 , 2005



Lehua Wang
Reg. No. 48,023

12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025-1026
(408) 720-8300

Substitute for Form 1449/PTO <h2 style="margin: 0;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="margin: 0; font-size: small;">(use as many sheets as necessary)</p>				Complete if Known	
				Application Number	10/758,977
				Filing Date	01/14/2004
				First Named Inventor:	McElvain
				Art Unit	2816
				Examiner Name	Not yet assigned
				Attorney Docket Number	2986.P045
Sheet	1	of	1		
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
		WEAVER, NICHOLAS; MARKOVSKIY, YURY; PATEL, YATISH; WAWRZYNEK, JOHN; "Post-Placement C-Slow Retiming for the Xilinx Virtex FPGA" 10 pages. Copyright 2003 ACM 1-58113-651 - X/03/0002 FPGA '03, February 23 - 25, Monterey, California USA.			
		NOTIFICATION TO PAY ADDITIONAL FEES for International Application No. PCT/US2004/010006 (Attorney Docket No. 2986.P031PCT), mailed 06/23/2005, 8 pages. European Patent Office, Rijswijk. Authorized Officer: Iveta Bujanska			
		SHALASH, Ahmad F., et al., "Power Efficient Fir Folding Transformation For Wireline Digital Communications", Copyright 1998 IEEE, 0-7803-5148-7/98, pages 1816-1820.			
		SUNDARARAJAN, Vijay, et al., "Synthesis of Low Power Folded Programmable Coefficient Fir Digital Filters", Copyright 2000, IEEE, 0-7803-5973-9/00, pages 153-156.			
		LIN, John, et al., "A New Multi-Algorithm Multichannel Cascadable Digital Filter Processor", IEEE 1988 Custom Integrated Circuits Conference, CH2584-1/88/0000-0060. Copyright 1988 IEEE. Pages 10.7.1 - 10.7.5.			
		HASSOUN, Soha, et al., "Architectural Retiming: Pipelining Latency-Constrained Circuits" Copyright 1996 ACM, Inc. 0-89791-833-9/96/0006, pages 708-713. 33 rd Design Automation Conference. XP-002330653			
		PARHI, Keshab K., et al., "Synthesis of Control Circuits in Folded Pipeline DSP Architectures", Copyright 1992 IEEE, 0018-9200/92503.00 Vol. 27, No. 1, January 1992. pages 29 - 43.			

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SENT FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.



FIRST CLASS CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail on the date indicated below with sufficient postage addressed to:

~~Mail Stop~~

Commissioner For Patents
P.O. Box 1450
Alexandria VA 22313-1450

Judy L. Steinkraus
Judy L. Steinkraus

07/12/2005

Application No.: 10/758,977	Filing Date: 01/14/2004	Docket No.: 2986.P045
Date Mailed: 07/12/2005	Due Date: 4/14/2004	Atty/Sec: JCS LW jxs
Client: Synplicity, Inc.		
Title: CIRCUITS WITH MODULAR REDUNDANCY AND METHODS AND APPARATUSES FOR THEIR AUTOMATED SYNTHESIS		
First Named Inventor: McElvain		
<i>The following has been received in the U.S.P.T.O. on the date stamped hereon:</i>		
Transmittal Letters & Certificate of Mailing		
<input type="checkbox"/> Transmittal Letter		
<input type="checkbox"/> Fee Transmittal (original & copy)		
<input type="checkbox"/> RCE (Request for Continued Examination)		
<input type="checkbox"/> Issue Fee Transmittal (original & copy)		
<input checked="" type="checkbox"/> Certificate of Mailing		
<input type="checkbox"/> Express Mail No.:		
Missing Parts, Formal Papers		
<input type="checkbox"/> Response to Notice of Missing Parts		
<input type="checkbox"/> Assignment & Cover sheet (____ pgs.)		
<input type="checkbox"/> Declaration & POA (____ pgs.)		
Amendment / Response		
<input type="checkbox"/> Amendment/Response (____ pgs.)		
<input type="checkbox"/> Examiner's Interview Summary		
<input checked="" type="checkbox"/> Other: copy of cited article		
Petitions & Appeals		
<input type="checkbox"/> Petition for Extension of Time:		
<input type="checkbox"/> Notice of Appeal		
<input type="checkbox"/> Appeal Brief & two copies (____ pgs. each)		
<input type="checkbox"/> Reply Brief (____ pgs.)		
Other		
<input checked="" type="checkbox"/> Information Disclosure Statement & PTO/SB/08 (____ pgs.) (previously 1449)		
<input type="checkbox"/> Terminal Disclaimer		
<input type="checkbox"/> Request to Publish (Rescind NonPublication)		
<input type="checkbox"/> Drawings: ____ sheets, ____ figures		
<input checked="" type="checkbox"/> Postcard		
Checks		
<input type="checkbox"/> Check No. _____ Amount \$ _____		